



THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re PATENT APPLICATION OF

INABA

Group Art Unit: 2822

Appln. No.: 09/977,933

Examiner: Unknown

Filed: October 17, 2001

Title: Semiconductor device and method of manufacturing the same

October 21, 2002

**INFORMATION DISCLOSURE STATEMENT**

Hon. Commissioner of Patents  
and Trademarks  
Washington, D.C. 20231

Sir:

I hereby certify that each enclosed document listed on the attached Form PTO-1449 was cited in the attached Japanese Patent Office Action, mailed September 24, 2002, issued in a corresponding Japanese patent application.


This Information Disclosure Statement is intended to be in full compliance with the rules, but should the Examiner find any part of its required content to have been omitted, prompt notice to that effect is earnestly solicited, along with additional time under Rule 97(f) to enable Applicant to comply fully.

Should a Final Rejection or Notice of Allowance have been issued before the filing date of this Information Disclosure Statement, please consider this a Petition under Rule 97(d)(ii), and charge the petition fee to our Deposit Account No. 03-3975, under Order No. 8312/284023, and proceed to consider this Information Disclosure Statement under rule 97(d).

Consideration of the foregoing and enclosures plus the return of a copy of the herewith Form PTO-1449 with the Examiner's initials in the left column per MPEP 609 is earnestly solicited.

Respectfully submitted,

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